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ATTENTION EXAMINER: PRENTY, MARK V

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Dinkar Singh et. al.

06/14/2005

Serial No.: 10/750,697

Group No.: 2822

Filed: 01/02/2004

Examiner: PRENTY, MARK V

**For: ENHANCED T-GATE STRUCTURE FOR MODULATION DOPED FIELD EFFECT
TRANSISTORS**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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